PATENT COOPERATION TREATY

PCT

TRANSLATION INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY (Chapter II of the Patent Cooperation Treaty)

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference P36023-P0				FOR FURTHER ACTION See Form PCT/IPEA/416		See Form PCT/IPEA/416				
International application No.				International filing da	nte (day/month/year)	Priority date (day/month/year)				
PCT/JP2004/016938				15.11.200		28.11.2003				
				 onal classification and						
H03D7/00 , H03D7/12, H03D7/14, H04B1/26										
1000,,00 , 1000,,12, 1000,,12, 10401,20										
Applicant										
MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.										
1.	 This report is the international preliminary examination report, established by this International Preliminary Examining Authority under Article 35 and transmitted to the applicant according to Article 36. 									
2.	This RI	EPORT consists	of a total of	7	sheets, including th	uis cover sheet.				
3.	This rep	port is also acco	mpanied by Al	NNEXES, comprising:						
	a	(sent to the	applicant and	to the International Bu	ureau) a total of	sheets, as follows:				
					-	amended and are the basis for this report and/or				
			containing red ctions).	tifications authorized	by this Authority (see R	ule 70.16 and Section 607 of the Administrative				
			-		-	nsiders contain an amendment that goes beyond				
		the di Box.	sclosure in the	international applicat	ion as filed, as indicated	d in item 4 of Box No. I and the Supplemental				
	ь. Г	(sent to the	International I	Bureau only) a total of	(indicate type and numb	er of electronic carrier(s))				
	related thereto, in electronic form only, as indicated in the Supplemental Box Relating to Sequence Listing (see Section									
		802 of the Ad	lministrative In	structions).						
4.	This rep	port contains in	dications relation	ng to the following iter	ns:					
	\boxtimes	Box No. I	Basis of the	report						
		Box No. II	Priority							
		Box No. III	Non-establis	shment of opinion with	regard to novelty, inven	ntive step and industrial applicability				
	П	Box No. IV	Lack of unit	v of invention						
<u> </u>					35(2) with regard to nov	elty, inventive step or industrial applicability;				
	citations and explanations supporting such statement									
	\bowtie	Box No. VI	Certain docu	aments cited						
	Ш	Box No. VII	Certain defe	cts in the international	application					
		Box No. VIII	Certain obse	ervations on the interna	ational application					
Date of submission of the demand					Date of completion of the	his report				
Name and mailing address of the IPEA/JP					Authorized officer					
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International application No.

PCT/JP2004/016938

Вох	No. I	Basis of the report							
1.	With	regard to the language, this report is based on:							
	\boxtimes	the international application in the language in which it was filed							
		the translation of the international application into translation furnished for the purposes of:	, which is the language of a						
		international search (Rule 12.3(a) and 23.1(b))							
		publication of the international application (Rule 12.4(a))							
		international preliminary examination (Rule 55.2(a) and/or	55.3(a))						
2.		egard to the elements of the international application, this report is based on (replacement sheets which have been furnished to the ag Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to cort):							
	\bowtie	the international application as originally filed/furnished	nternational application as originally filed/furnished						
	Ш	the description:							
		pages	as originally filed/furnished						
		pages* rec	eived by this Authority on						
		pages* rec	eived by this Authority on						
		the claims:							
		nos.	as originally filed/furnished						
		nos.*	as amended (together with any statement) under Article 19						
			eived by this Authority on						
			eived by this Authority on						
		the drawings:	· · · · · · · · · · · · · · · · · · ·						
		· ·	as originally filed/furnished						
			eived by this Authority on						
			eived by this Authority on						
			· · · · ·						
	H	a sequence listing and/or any related table(s) – see Supplemental	Box Relating to Sequence Listing.						
3.	Ш	The amendments have resulted in the cancellation of:							
		the description, pages	the description, pages						
		the claims, nos.	the claims, nos.						
		the drawings, sheets/figs	the drawings, sheets/figs						
		the sequence listing (specify):							
		any table(s) related to sequence listing (specify):							
4.		This report has been established as if (some of) the amendment they have been considered to go beyond the disclosure as filed, a	s annexed to this report and listed below had not been made, since s indicated in the Supplemental Box (Rule 70.2(c)).						
		the description, pages							
		the claims, nos.	the claims, nos.						
		the drawings, sheets/figs	the drawings, sheets/figs						
		the sequence listing (specify):							
		any table(s) related to sequence listing (specify):							
*	If ite	m 4 applies, some or all of those sheets may be marked "superseded."							

2. Citations and explanations (Rule 70.7)

Document 1: JP 04-129407 A (Alps Electric Co., Ltd.), 30

April 1992

Document 2: JP 07-254821 A (NEC Corp.), 03 October 1995

Document 3: JP 2001-522566 A (Maxim Integrated Products, Inc.), 13 November 2001

Document 4: JP 09-069730 A (NEC Corp.), 11 March 1997

The inventions set forth in claims 1, 3, 9 and 12 are the same as the configurations illustrated in fig. 1 to 3 of document 1 cited in the international search report. In addition, the invention set forth in claim 14 is the same as the configuration illustrated in fig. 2 of document 1 cited in the international search report.

Such being the case, the inventions set forth in claims 1, 3, 9, 12 and 14 lack novelty and do not involve an inventive step in the light of document 1 cited in the international search report.

Document 1 cited in the international search report indicates that "in order to improve the distortion characteristics, it is necessary to supply a large collector current to the differential amplifier comprising transistors Q5 and Q6; on the other hand, in order to improve the noise factor characteristics of the

Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

double balanced mixer comprising transistors Q1, Q2, Q3 and Q4, it is necessary to decrease the collector current" (refer to page 2, lower left column, lines 4 to 9).

Meanwhile, document 2 cited in the international search report discloses a configuration wherein the transistors are connected in parallel in order to reduce the current that flows therethrough.

Such being the case, the inventions set forth in claims 2, 10 and 11 are obvious in the light of documents 1 and 2 cited in the international search report; consequently, the inventions in question do not involve an inventive step.

Document 3 cited in the international search report describes the technical concept of providing bias current supply circuits, each of which only supplies an additional bias current to a respective output load unit, instead of using a single bias current supply circuit to supply a bias current to all of the output load units.

Such being the case, the invention set forth in claim 4 is obvious in the light of documents 1 and 3 cited in the international search report; consequently, the invention in question does not involve an inventive step.

Document 4 cited in the international search report disclosed a double balanced mixer circuit (fig. 1) and a single balanced mixer circuit (fig. 2). Given the similar configurations of the mixer circuits in question, it would have been obvious to a person skilled in the art to configure a single balanced mixer circuit from the mixer circuit illustrated in fig. 1 to 3 of document 1 cited in the international search report.

Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

Such being the case, the inventions set forth in claims 5 and 7 are obvious in the light of documents 1 and 4 cited in the international search report; consequently, the inventions in question do not involve an inventive step.

The configuration that involves a single balanced type mixer circuit is obvious for the same reasons as the inventions set forth in claims 5 and 7, while the configuration wherein the transistors are connected in parallel is obvious for the same reasons as the inventions set forth in claims 2, 10 and 11.

Such being the case, the invention set forth in claim 6 is obvious in the light of documents 1, 2 and 4 cited in the international search report; consequently, the invention in question does not involve an inventive step.

The provision of current sources, each of which only supplies an additional current to a respective load resistor, is not disclosed or suggested in any of the documents that are cited in the international search report.

Consequently, the inventions set forth in claims 8 and 13 involve an inventive step.

The substitution of a MOS transistor for a bipolar transistor would be an obvious substitution for a person skilled in the art to make.

Such being the case, the invention set forth in claim 15 is obvious in the light of document 1 cited in the international search report; consequently, the invention in question does not involve an inventive step.

The configuration wherein the mixer circuit is employed in a direct conversion receiver system or in a

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Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement							
low-IF receiver system would be obvious to a person							
skilled in the art.							
Such being the case, the invention set forth in							
claim 16 is obvious in the light of document 1 cited in							
the international search report; consequently, the							
invention in question does not involve an inventive step.							

International application No.
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Box No.	.VI Certain documents cited				
1. Ce	ertain published documents (Rule 70.10)				
	Application No. Patent No.	Publication date (day/month/year)	Filing date (day/month/year)	Priority date (valid claim) (day/month/year)	
	JP 2004-104515 A	02.04.2004	10.09.2002		
	[P,X]				
2. No	on-written disclosures (Rule 70.9)				
				e of written disclosure	
	Kind of non-written disclosure	Date of non-written di (day/month/yea	isclosure referringer)	referring to non-written disclosure (day/month/year)	